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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Lap-Wai Chow)	Group Art No.:	2814
Application No:	10/637,848)	Examiner:	Cao, Phat X
Filed:	08/07/2003)	Re:	Response
For: "Use of Silicon Block Process Step...")	Our Ref:	B-4505 NP 620843-6
)	Date:	August 16, 2004

Commissioner for Patents
POB 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is filed in response to the official action dated July 22, 2004.

Remarks begin on page 2 of this paper.